

FIG. 1



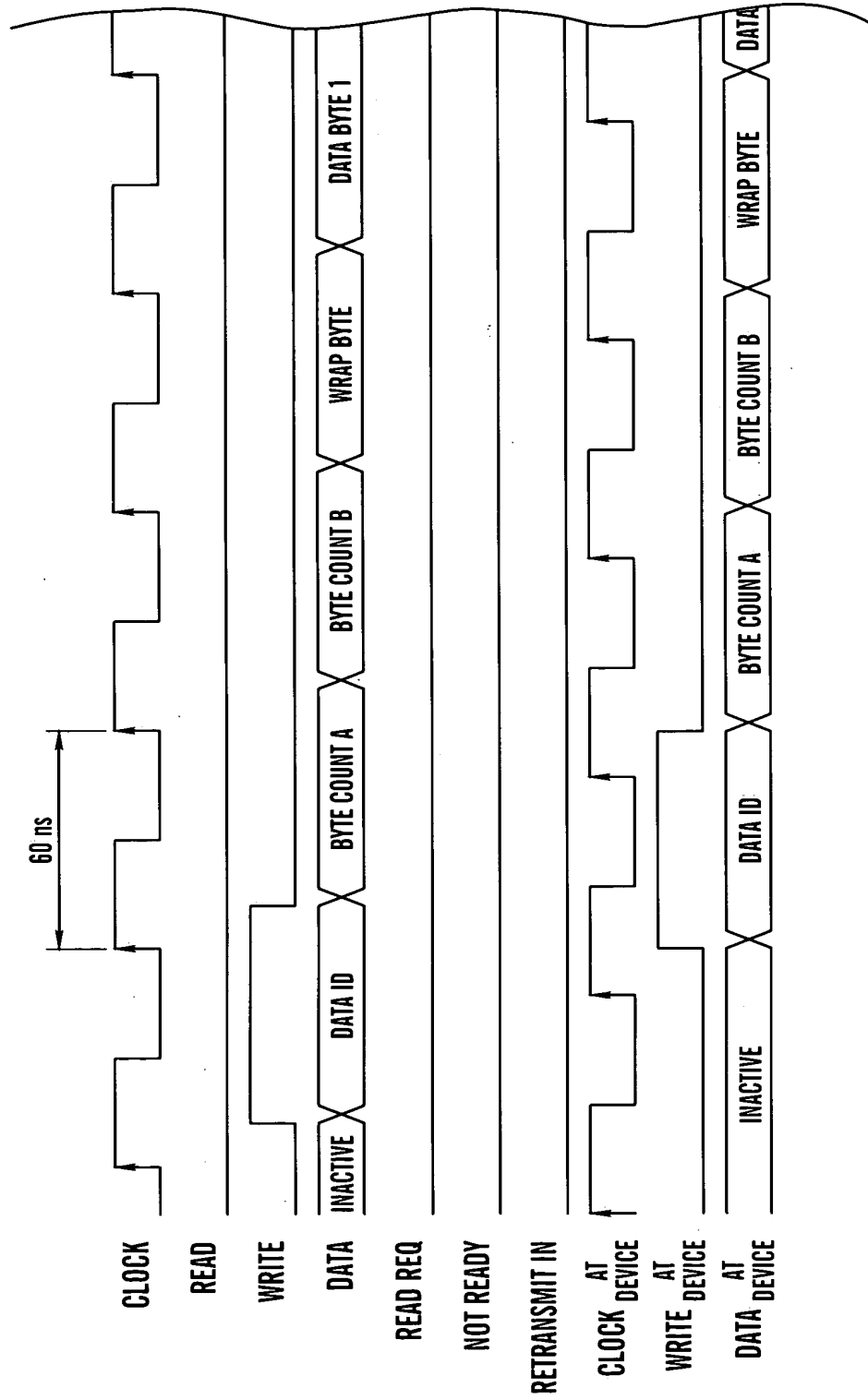


FIG. 2





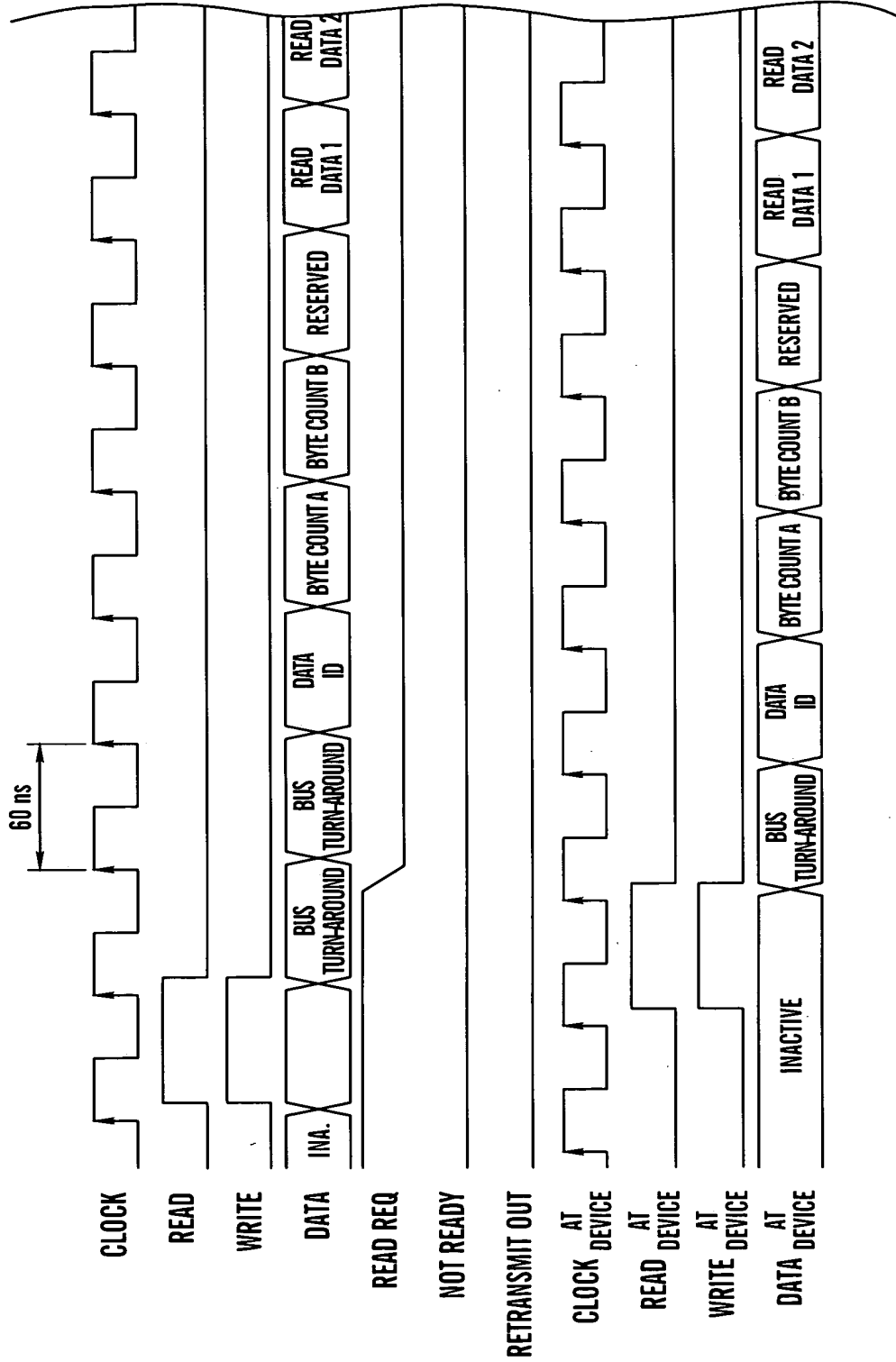


FIG. 4



OFFSET ADDRESS:	00000h	TCL-1 READ REGISTERS	0K	
	04000h	TCL-1 WRITE REGION	16K	
	08000h	TCL-2 READ REGISTERS	32K	
	0C000h	TCL-2 WRITE REGION	48K	
	10000h	TCL-3 READ REGISTERS	64K	
	14000h	TCL-3 WRITE REGION	80K	256K DUAL-PORTED SRAM
	18000h	TCL-4 READ REGISTERS	96K	
	1C000h	TCL-4 WRITE REGION	112K	
	20000h	MICR SAMPLE DATA STORAGE REGION A	128K	
	30000h	MICR SAMPLE DATA STORAGE REGION B	192K	
	40000h	TRANSFER CONTROL AND STATUS REGISTERS	256K	
	40400h	UNUSED REGION	257K	
			511K	

FIG. 5



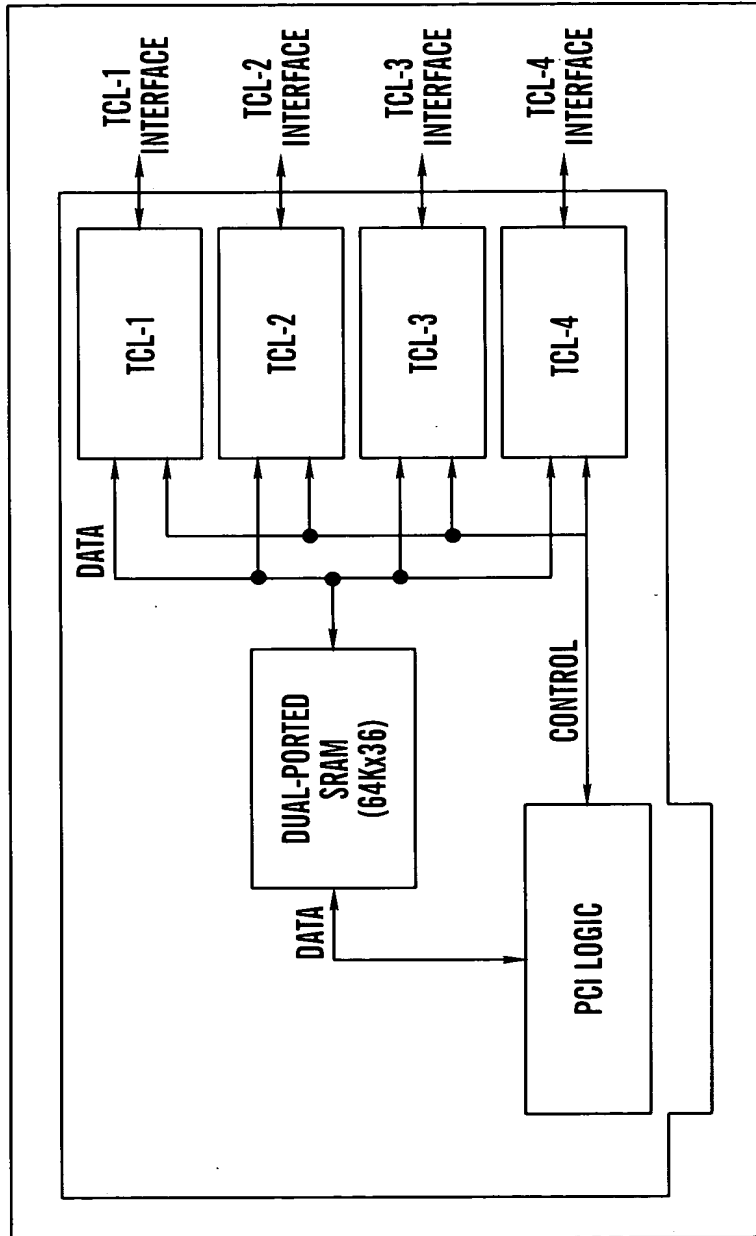


FIG. 6



REGION 0 REGION 1 REGION 2 REGION 3 REGION 4 REGION 5 REGION 6 REGION 7 REGION 8 REGION 9 REGION A REGION B REGION C REGION D REGION E REGION F	WRITE TRANSFER RECORD START LOCATIONS			
	TCL-1 WRITE REGIONS	TCL-2 WRITE REGIONS	TCL-3 WRITE REGIONS	TCL-4 WRITE REGIONS
	0 - 04000h 1 - 04400h 2 - 04800h 3 - 04C00h 4 - 05000h 5 - 05400h 6 - 05800h 7 - 05C00h 8 - 06000h 9 - 06400h A - 06800h B - 06C00h C - 07000h D - 07400h E - 07800h F - 07C00h	0 - 0C000h 1 - 0C400h 2 - 0C800h 3 - 0CC00h 4 - 0D000h 5 - 0D400h 6 - 0D800h 7 - 0DC00h 8 - 0E000h 9 - 0E400h A - 0E800h B - 0EC00h C - 0F000h D - 0F400h E - 0F800h F - 0FC00h	0 - 14000h 1 - 14400h 2 - 14800h 3 - 14C00h 4 - 15000h 5 - 15400h 6 - 15800h 7 - 15C00h 8 - 16000h 9 - 16400h A - 16800h B - 16C00h C - 17000h D - 17400h E - 17800h F - 17C00h	0 - 1C000h 1 - 1C400h 2 - 1C800h 3 - 1CC00h 4 - 1D000h 5 - 1D400h 6 - 1D800h 7 - 1DC00h 8 - 1E000h 9 - 1E400h A - 1E800h B - 1EC00h C - 1F000h D - 1F400h E - 1F800h F - 1FC00h

(DPM OFFSET ADDRESS)

DEVICE WRITE
REGIONS

FIG. 7



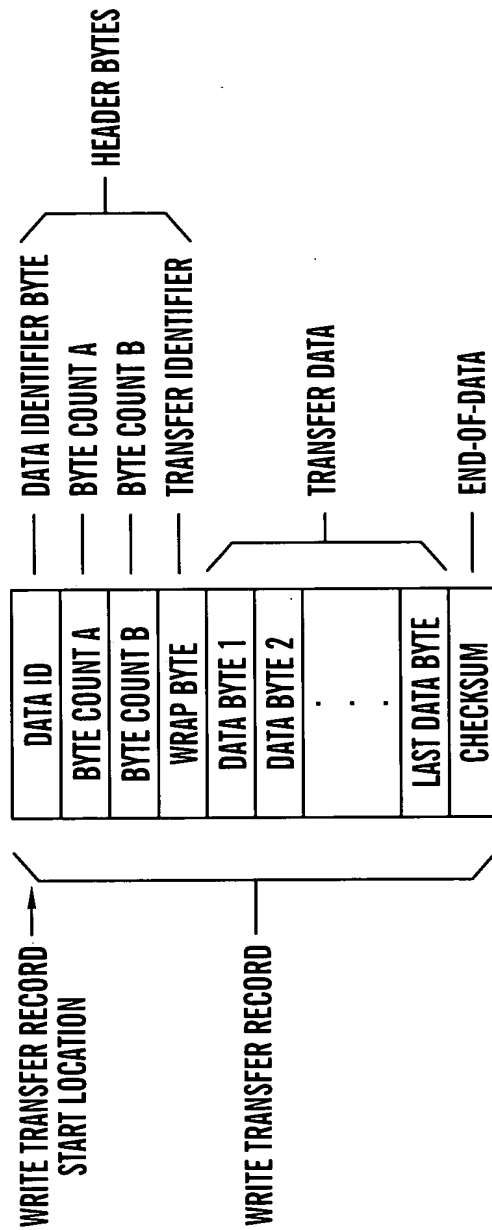


FIG. 8



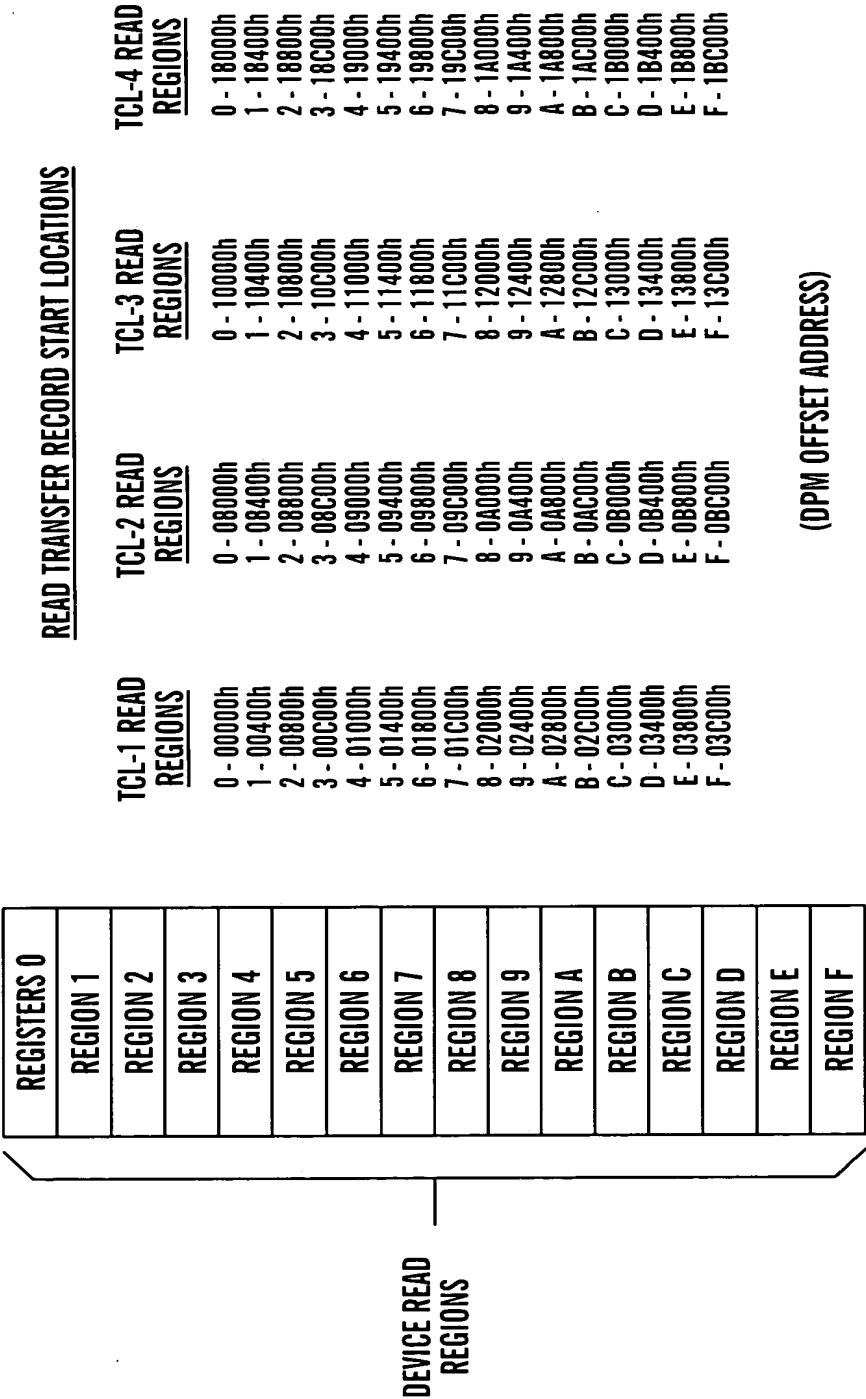


FIG. 9



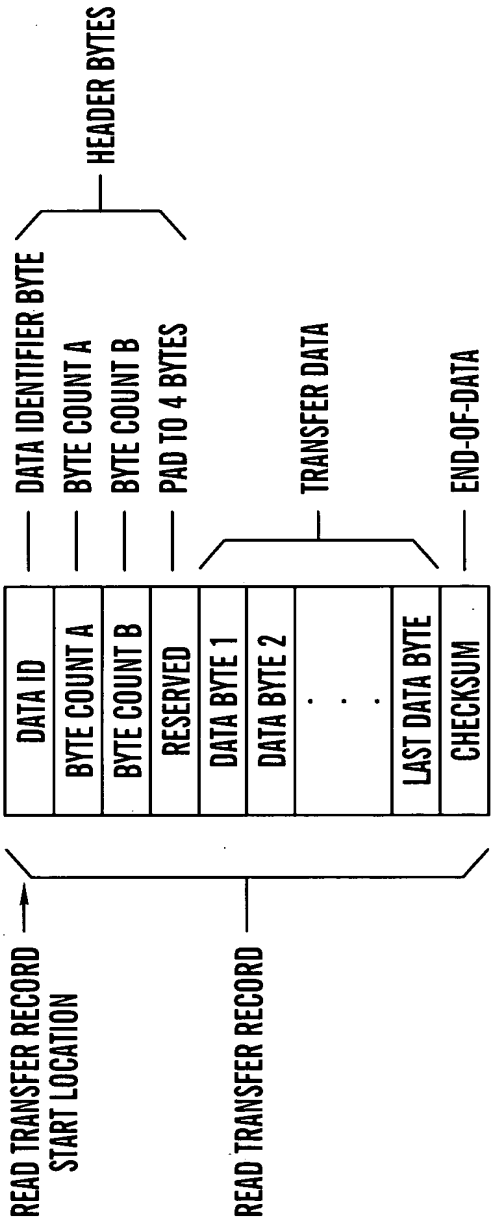


FIG. 10

